EE1100 Basic Electrical Engineering – Teaching Plan @ IITP for Aug-Nov 2016

Topics	# hours
Introduction + Topic 1 :- Basics and definitions: definition of charge, voltage, current, power,	2
energy, resistor, Ohm's law, ideal sources, internal resistance of voltage and current sources	
Topic 2 :- Circuit laws and solving for unknowns: description & derivation of Kirchoff's laws,	2
series and parallel connections, analysis of circuits using Nodal analysis technique usage of	
the principle of linearity and superposition to solve when multiple sources are present.	
Topic 3 :- Dynamic circuit components L & C:derivation of the relationship between current	4
and voltage in capacitors(C) and inductors(L), power in L & C, combination of R & L , R & C,	
numerical & graphical analysis of transients with DC sources, combination of R, L & C in	
circuits.	
Topic 4 :- Single phase AC circuits :	5 to 6
Introduction to AC signals and sources, description of AC signals using magnitude, frequency	
and phase, distinguishing between peak, average and RMS values, definition of power in AC	
circuits, derivation of properties of R, L and C with AC signals, introduction to Impedance(Z),	
Differentiate between DC and steady state, calculation of power in AC systems,	
differentiating between active, reactive and apparent power, introduction to power factor,	
usage of phasors to represent, manipulate and calculate circuit quantities like voltage,	
current, impedance, and power.	
Quiz I, Thursday, September 8	
Topic 5 :- How does the wireline telephone system work?	6
Local loop, analog to digital conversion, sampling of speech, compression of speech, echo-	
control, blocking and non-blocking switches, application of Erlang-B formula to voice	
networks, and how to send data thro the (voice-based) telephone network	
Topic 6 :- Digital communications	5 to 6
Analog communications vs Digital communication, examples from source compression, error-	
control coding, mapping bits to waveforms (transmit modulation), and recovering bits from	
noise corrupted waveforms (demodulation and detection at the receiver)	
Quiz II, Thursday, October 20	2
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Breakup of Assessment:

3 mini-quizzes of 5 marks each; Quiz1 : 15 marks; Quiz2: 20 marks; Final: 50 marks

Books:

- > Electrical Circuit Theory and Technology, John Bird, Elsevier, 2011 [Text book for topics 1 to 4]
- > Essentials of Electrical and Computer Engineering, Kerns & Irwin, Pearson, 2004
- > Notes on the remaining topics will be uploaded on Moodle